

FORM 1449*

INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number:

50019.238US01

Application Number:

10/657,393

Applicant: John Patrick Fairbanks

Filing Date: September 8,
2003

Group Art Unit: 2816



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JZ		"LM2623 "Ratio Adaptive, Gated Oscillator Cookbook," <i>National Semiconductor Corporation</i> , April 2002 (5 pages)
JZ		"LM2623 Ratio Adaptive Gated Oscillator Based, DC DC Boost Converter-Low Vin, Low Ripple, General Purpose," <i>National Semiconductor Corporation</i> , November 2002 (11 pages)

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PATENT TRADEMARK OFFICE

EXAMINER Jeffrey Zweizig

DATE CONSIDERED 9/15/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.